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Frame bit size allocation e.g. for seamlessly spliced, variable rate compressed digital video signals, involves determining, prior to encoding current frame, maximum bit size for current frame

Patent Assignee: GEN INSTR CORP (GENN ); CHEN J (CHEN-I); LIU V (LIUV-I); WU S (WUSS-I)

Inventor: CHEN J; LIU V; WU S

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Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 1217842	A2	20020626	EP 2001310432	A	20011213	200257 B
CA 2365365	A1	20020621	CA 2365365	A	20011218	200257
US 20020118747	A1	20020829	US 2000746347	A	20001221	200259
CN 1365235	A	20020821	CN 2001144796	A	20011221	200281
KR 2002050728	A	20020627	KR 200181698	A	20011220	200282

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Patent Details:

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EP 1217842	A2	E	16	H04N-007/24	
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CA 2365365	A1	E		H04N-007/26	
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CN 1365235	A			H04N-007/26	
KR 2002050728	A			H04N-007/24	

Abstract (Basic): EP 1217842 A2

Abstract (Basic):

NOVELTY - The method involves determining, prior to encoding the current frame, a maximum bit size for the current frame preventing an underflow of the current frame from the decoder buffer to the decoder.

DETAILED DESCRIPTION - A bit size is allocated for the current frame in accordance with the determined maximum bit size for the current frame. When the second signal has a variable bit-encoding rate and the current frame is not decoded until after the set switching time, the **\*\*maximum\*\*** bit **\*\*size\*\*** is determined in accordance with an **\*\*estimate\*\*** of the decoder **\*\*buffer\*\*** fullness at the set switching time. An INDEPENDENT CLAIM is included for a method of allocating a bit size for a current frame in a group of pictures, a method of allocating a bit budget for a current frame, and a computer readable **\*\*storage\*\*** medium.

USE - For seamlessly spliced, variable rate compressed digital video signals

ADVANTAGE - Allocates bit size for current frame in group of pictures.

DESCRIPTION OF DRAWING(S) - The figure shows a diagram showing various parameters that are processed in different stages of the